

35. The method of digital amplification of claim 34, wherein a semiconductor H-bridge controller generates the multi-state output signal.

36. The method of digital amplification of claim 35, wherein the sampling includes generating a sampled output signal having a lower transition rate with respect to a sampling frequency by a predetermined multiple.

37. The method of digital amplification of claim 36, wherein the sampling includes suppressing sampling of the noise shaped signal for a set number of clock cycles of a sampling frequency clock.

38. The method of digital amplification of claim 37, wherein the suppressing sampling includes detecting a transition in the output signal.

39. The method of digital amplification of claim 38, wherein the noise shaping includes integrating the summed output signal through a plurality of integrator stages.

REMARKS

The above identified patent application has been amended and reconsideration and reexamination are hereby requested.

Claims 14 - 25 and 28 - 39 are now in the application. Claims 4 - 6, 11 - 13 and 26 - 27 have been cancelled.

The Examiner has rejected Claims 4 - 6, 11 - 13 and 26 - 27 under 35 U.S.C. §102(b) as being anticipated by Adrian et al. and under 35 U.S.C. §103 as being unpatentable over Howatt. However, the Examiner has allowed Claims 14 - 25 and 28 - 39.

The Applicants have canceled Claims 4 - 6, 11 - 13 and 26 - 27.

The Applicants are also submitting concurrently herewith an accompanying Information Disclosure Statement (IDS) pursuant to the above-mentioned Request for Continued Examination.

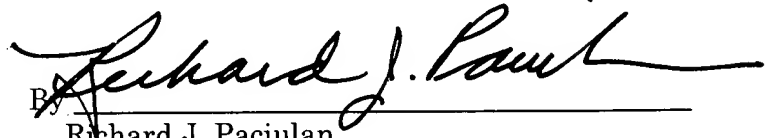
Accordingly, in view of the above amendment it is submitted that all the rejections to the claims have been overcome and that the Application is in condition for allowance, pending the

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Examiner's consideration of the references submitted in the accompanying IDS. Reconsideration and reexamination of the above Application is hereby requested.

Respectfully submitted,

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